

LAW OFFICES OF
ZAGORIN, O'BRIEN & GRAHAM, L.L.P.

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AUSTIN, TEXAS 78701

INTELLECTUAL PROPERTY ATTORNEYS

(512) 347-9030 (PHONE)
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#3
Letter
7-16-02

July 16, 2002

Office of Finance
Refund Section
P.O. Box 16
Washington D.C. 20231

RE: Deposit Account No. 500631

Dear Sir or Madam:

On April 4, 2002, our firm filed an Information Disclosure Statement for Application No. 10/015,030 and Application No. 09/949,493 (copies enclosed). Because the Information Disclosure Statements were filed before the mailing of a first office action, no fee was believed necessary.

Upon receipt of our monthly Deposit Account statement ending April 30, 2002, a charge of \$180 (Fee Code 126) was made to our Deposit Account for Application No. 10/015,030 and Application No. 09/949,493. Since no fee was necessary at the time of filing the Information Disclosure Statements, please credit our Deposit Account the amount of \$360.

If you have any questions or require additional information or documentation, please do not hesitate to contact my assistant, Peggy S. Pepitone or me.

Very truly yours,



Mark Zagorin

MZ/psp

Enclosures

RECEIVED
JUL 16 2002
U.S. PATENT & TRADEMARK OFFICE
WASHINGTON, D.C. 20503

Commissioner for Patents
Washington, DC 20231

Applicant(s): Philip D. Steiner et al.	
Title: METHOD AND APPARATUS FOR DETERMINING A LOSS OF SIGNAL CONDITION	
Application No.: 10/015,030	Filed: December 11, 2001
Atty. Dkt. No.: 026-0013	

- (1) Information Disclosure Statement (2 page(s)) with
☒ Form(s) PTO-1449 (1 page(s))
☒ Copies of two references
☐ Other: N/A (____ page(s))
- (2) This Return Postcard

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Mailing Date: April 4, 2002

Commissioner for Patents
Washington, DC 20231

DOCKETED RECEIVED
ZAGORIN O'BRIEN & GRAHAM, LLP.
APR 25 2002 APR 22 2002

Applicant(s): Philip D. Steiner et al.	
Title: METHOD AND APPARATUS FOR DETERMINING A LOSS OF SIGNAL CONDITION	
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☒ Copies of two references
☐ Other: N/A (____ page(s))
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Mailing Date: April 4, 2002

IN THE UNITED STATES PATENT AND TRADEMARK OFFICE

Applicant(s): Philip D. Steiner et al.

Title: METHOD AND APPARATUS FOR DETERMINING A LOSS OF
SIGNAL CONDITION

Application No.: 10/015,030

Filed: December 11, 2001

Examiner: Unknown

Group Art Unit: 2133

Atty. Docket No.: 026-0013

April 4, 2002

COMMISSIONER FOR PATENTS
Washington, DC 20231**INFORMATION DISCLOSURE STATEMENT
UNDER 37 C.F.R. § 1.97**

Dear Sir:

Pursuant to 37 C.F.R. § 1.56, § 1.97 and § 1.98, the undersigned brings the patents, publications, applications or other information identified in the attached:

- ☒ Form(s) PTO-1449
☐ Other: n/a

to the Examiner's attention in the above-identified application. Citation of such information shall not be construed as:

1. an admission that the information necessarily is, or corresponds to, prior art with respect to the instant invention;
2. a representation that a search has been made, other than as described below; or
3. an admission that the information cited herein is, or is considered to be, material to patentability as defined in § 1.56(b).

For each item of information listed that is not in the English language, the undersigned has provided a concise explanation of the relevance through (i) an English language abstract, (ii) an English language equivalent application, or (iii) if cited in a search report or other action by a foreign patent office in a counterpart foreign application, an English language version of the search report or action that indicates the degree of relevance found by the foreign office.

FEE AUTHORIZATION

- ☐ This Information Disclosure Statement is filed within three months of the filing date of a national application other than a continued prosecution application under § 1.53(d) or within three months of entry of the national stage as set forth in § 1.491 in an international application. Therefore, no fee is required.
- ☒ The undersigned believes that this Information Disclosure Statement is being filed before the mailing date of a first Office action on the merits or before the mailing date of a first Office action after the filing of a request for continued examination under § 1.114. Therefore, no fee is believed required.
- ☒ If however, this Information Disclosure Statement is filed after the period specified in § 1.97(b), the undersigned hereby authorizes the Commissioner to charge the fee set forth in § 1.17(p) to Deposit Account No. 50-0631.

CERTIFICATE OF MAILING

I hereby certify that, on the date shown below, this correspondence is being deposited with the United States Postal Service with sufficient postage as First Class Mail in an envelope addressed to: Commissioner for Patents, Washington, DC 20231.

Mark Zagorin
Mark Zagorin

4/4/02
Date

Respectfully submitted,

Mark Zagorin

Mark Zagorin, Reg. No. 36,067
Attorney for Applicant(s)
(512) 347-9030
(512) 347-9031 (fax)

U.S. Department of Commerce, Patent and Trademark Office				Attorney Docket No.: 026-0013	
				Application No.: 10/015,030	
INFORMATION DISCLOSURE STATEMENT BY APPLICANT				Applicant(s): Philip D. Steiner et al.	
(Use several sheets if necessary)				Filing Date: December 11, 2001	
				Group Art Unit: 2133	
				Date Submitted: April 4, 2002	
U.S. Patent Documents					
*Examiner Initial		Document Number	Date	Name	
	AA				
	AB				
	AC				
	AD				
	AE				
	AF				
	AG				
	AH				
	AI				
	AJ				
	AK				
Foreign Patent Documents					
		Document	Date	Country	Translation
					Yes No
	AL				
	AM				
	AN				
	AP				
OTHER ART (Including Author, Title, Date, Pertinent Pages, Etc.)					
	AR	Giga, "2.5 Gbit/s Clock and Data Recovery GD16522", Data Sheet Rev. 20, Giga, September 25, 2000, pp. 1-11			
	AS	Maxim, "2.5Gbps, Low-Power, +3.3V Clock Recovery and Data Retiming IC", Maxim Integrated Products, 1998, pp. 1-8			
	AT				
Examiner		Date Considered			
*EXAMINER: Initial if reference considered, whether or not citation is in conformance with MPEP 609; Draw line through citation if not in conformance and not considered. Include copy of this form with your communication to applicant.					

Commissioner for Patents
Washington, DC 20231

Applicant(s):	Philip D. Steiner and Michael H. Perrott		
Title:	CALIBRATION OF A LOSS OF SIGNAL DETECTION SYSTEM		
Application No.:	09/949,493	Filed:	September 7, 2001
Atty. Dkt. No.:	026-0016		

(1) Information Disclosure Statement (2 page(s)) with

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☒ Copies of two references
☐ Other: N/A (_____ page(s))

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Mailing Date: April 4, 2002

Commissioner for Patents
Washington, DC 20231

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APR 25 2002

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ZAGORIN O'BRIEN & GRAHAM, L.L.P.
APR 22 2002

Applicant(s):	Philip D. Steiner and Michael H. Perrott		
Title:	CALIBRATION OF A LOSS OF SIGNAL DETECTION SYSTEM		
Application No.:	09/949,493	Filed:	September 7, 2001
Atty. Dkt. No.:	026-0016		

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Mailing Date: April 4, 2002

IN THE UNITED STATES PATENT AND TRADEMARK OFFICE

Applicant(s): Philip D. Steiner and Michael H. Perrott
Title: CALIBRATION OF A LOSS OF SIGNAL DETECTION SYSTEM
Application No.: 09/949,493 Filed: September 7, 2001
Examiner: Unknown Group Art Unit: 2857
Atty. Docket No.: 026-0016

April 4, 2002

COMMISSIONER FOR PATENTS
Washington, DC 20231

**INFORMATION DISCLOSURE STATEMENT
UNDER 37 C.F.R. § 1.97**

Dear Sir:

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- ☒ Form(s) PTO-1449
☐ Other: n/a

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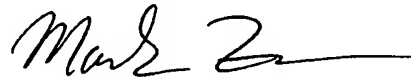
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Mark Zagorin

4/4/02
Date

Respectfully submitted,



Mark Zagorin, Reg. No. 36,067
Attorney for Applicant(s)
(512) 347-9030
(512) 347-9031 (fax)

U.S. Department of Commerce, Patent and Trademark Office				Attorney Docket No.: 026-0016	
INFORMATION DISCLOSURE STATEMENT BY APPLICANT (Use several sheets if necessary)				Application No.: 09/949,493	
				Applicant(s): Philip D. Steiner and Michael H. Perrott	
				Filing Date: September 7, 2001	
				Group Art Unit: 2857	
				Date Submitted: April 4, 2002	
U.S. Patent Documents					
*Examiner Initial		Document Number	Date	Name	
	AA				
	AB				
	AC				
	AD				
	AE				
	AF				
	AG				
	AH				
	AI				
	AJ				
	AK				
Foreign Patent Documents					
		Document	Date	Country	Translation Yes No
	AL				
	AM				
	AN				
	AP				
OTHER ART (Including Author, Title, Date, Pertinent Pages, Etc.)					
	AR	Giga, "2.5 Gbit/s Clock and Data Recovery GD16522", Data Sheet Rev. 20, Giga, September 25, 2000, pp. 1-11			
	AS	Maxim, "2.5Gbps, Low-Power, +3.3V Clock Recovery and Data Retiming IC", Maxim Integrated Products, 1998, pp. 1-8			
	AT				
Examiner			Date Considered		
*EXAMINER: Initial if reference considered, whether or not citation is in conformance with MPEP 609; Draw line through citation if not in conformance and not considered. Include copy of this form with your communication to applicant.					